Fig. C3.1  CONTROL LOGIC SIMPLIFIED BLOCK DIAGRAM
Fig. C98Y  ELECTRONICS UNIT INTERCONNECTIONS
(Early model wiring differences)
Fig. C7Z  LAMP DRIVE, BIAS TRAPS & ABC SOURCE
BP/C/60810/P2
Fig. C12Z  LINE DRIVE AMPLIFIER  BP/C/60551/P2
Fig. C9Z  MICROPHONE AMPLIFIER  BP/C/60816/P3
Fig. C13Z  MONITOR AMPLIFIER  BP/C/60407/P2
Fig. C1Z  REGULATED POWER SUPPLIES
(Part of Deck Rear PCB)
Fig. S1Y  SIGNAL CIRCUITS (Block Diagram)
Fig. C8Z  BIAS AND ERASE OSCILLATOR  BP/B/60409/P2
Fig. C99.1  LEFT-HAND CAPACITOR BRACKET
Fig. C99.2  RIGHT-HAND CAPACITOR BRACKET & MOTOR DRIVE PCB
Fig. C5.4  CAPSTAN SERVO AMPLIFIER, PCB LAYOUT
Fig. C3.2 CONTROL UNIT & ELAPSED TIME INDICATOR, PCB LAYOUT
TR381 from the start-up surge, TR379 provides feedback to limit the current through the lamp LP372.

A, B, C Photo-Transistor PCB (part of Counter Wheel Assembly)

Fig. C7.1 LAMP DRIVE, BIAS TRAPS & A, B, C SOURCE, PCB LAYOUT
Fig. C12.1  LINE DRIVE AMPLIFIER, PCB LAYOUT
Transformer connections

**T731A**
- Black to Pin 10
- Black to Pin 10
- Green to Pin 10
- Blue to Pin 6
- Yellow to Pin 7
- Red to Pin 8
- Orange to Pin 8
- All wires twisted from can to pins

**T731B**
- Black to Pin 1
- Black to Pin 1
- Green to Pin 1
- Blue to Pin 2
- Yellow to Pin 3
- Brown to Pin 4
- Red to Pin 5
- Orange to Pin 5
- All wires twisted from can to pins

**Fig. C9.1**
MICROPHONE AMPLIFIER, PCB LAYOUT
C1.2 SPOOLING SERVO, MOTOR CONTROL & REGULATED POWER SUPPLIES, PCB LAYOUT
Fig. C10.2  RECORD AMPLIFIER, PCB LAYOUT
Fig. C11.1
REPLAY AMPLIFIER, PCB LAYOUT

<table>
<thead>
<tr>
<th>R653: Pins 10-13</th>
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<tr>
<td>R654: Pins 2-5</td>
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<td>R656: Pins 8-11</td>
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<td>R657: Pins 4-7</td>
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Link: Pins 1 & 6
Link: Pins 9 & 14

* Equalisation (Replay) Module
Fig. C8.2  BIAS & ERASE OSCILLATOR, PCB LAYOUT