SERVICE MANUAL

INTEGRATED STEREO AMPLIFIER

SANSUI AU-719

SPECIFICATIONS

Power output
Min. RMS, both channels driven, from 10 to 20,000 Hz,
with no more than 0.015% total harmonic distortion
90 watts per channel into 8 ohms
Load impedance ............... 8 ohms
Total harmonic distortion .......... less than 0.015% at or below rated
min. RMS power output
Intermodulation distortion (70 Hz; 7 kHz = 4:1 SMPTE
method) .......... less than 0.015% at or below rated
min. RMS power output
Rise time ............... 0.5 microsec
Slew rate ............... ±170 V/µsec
Frequency response (at 1 watt)
DC to 400,000 Hz, +3 dB, -6 dB
Damping factor (1 kHz, both channels driven)
........... 110 into 8 ohms
RIAA curve deviation (PHONO, 20 Hz to 20 kHz)
........... +0.2 dB, -0.2 dB
Input sensitivity and impedance (1 kHz, for rated power
output)
PHONO-1, 2 .......... 2.5 mV/47 kilohms
(Max. input capability; 230 mV at 1 kHz, less than
0.01% total harmonic distortion)
AUX, TUNER, TAPE PLAY
........... 200 mV/47 kilohms
Output level and impedance (1,000 Hz)
TAPE REC .......... 200 mV into 47 kilohms/600 ohms
Channel separation (1 kHz, at rated power output)
PHONO-1, 2 .......... 65 dB
AUX, TUNER, TAPE PLAY
........... 70 dB
Hum and noise (short-circuit, A-network)
PHONO-1, 2 .......... 88 dB
AUX, TUNER, TAPE PLAY
........... 100 dB

Controls
BASS ............... ±10 dB (50 Hz)
Tone selector ....... 15 Hz, 300 Hz
TREBLE .......... ±10 dB (15 kHz)
Tone selector ....... 3 kHz, 6 kHz
SUBSONIC .......... -3 dB (16 Hz), 6 dB/oct
MUTING .......... -20 dB
LOUDNESS (VOLUME control: -30 dB position)
........... 10 dB at 50 Hz
........... 6 dB at 10 kHz

Power requirements
Power voltage .......... 100, 120, 220, 240 V (50/60 Hz)
For U.S.A. & Canada
........... 120 V (60 Hz)
Power consumption
Rated consumption
........... 450 watts 530 VA
Dimensions .......... 430 mm (16-11/16") W
........... 168 mm (6-7/8") H
........... 395 mm (15-9/16") D

Weight .......... 16.0 kg (35.3 lbs) net
........... 17.7 kg (39.0 lbs) packed

* Design and specifications subject to changes without
notice for improvements.
* In order to simplify the explanation illustrations may
sometimes differ from the originals.
1. BLOCK DIAGRAM

2. OPERATION BLOCK DIAGRAM OF DRIVER STAGE
3. ADJUSTMENTS

Notes: 1. Master Volume .... Minimum
2. Room Temperature .... 18°C ~ 28°C
(65°F ~ 83°F)
3. When replacing some parts or circuit board, refer to description in REMARKS.
4. For this adjustment, run the unit for more than 3 minutes after the power is switched ON.

3-1. Driver Circuit (F-2926, F-2927) Adjustment (See Fig. 3-1 & 3-2)

<table>
<thead>
<tr>
<th>STEP</th>
<th>SUBJECT</th>
<th>MEASURE OUTPUT</th>
<th>ADJUST</th>
<th>ADJUST FOR</th>
<th>REMARKS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>DC 0V Adj.</td>
<td>Speaker terminal (L-ch)</td>
<td>VR01 (L-ch) F-2926</td>
<td>DC 0V ±5 mV</td>
<td>Before turning ON power switch, set VR01 to center position.</td>
</tr>
<tr>
<td></td>
<td>L-ch</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.</td>
<td>DC 0V Adj.</td>
<td>Speaker terminal (R-ch)</td>
<td>VR01 (R-ch) F-2927</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>R-ch</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>3.</td>
<td>Bias current</td>
<td>Between emitters of power transistors, TR701 &amp; TR703 (See Fig. 3-1)</td>
<td>VR02 (L-ch) F-2926</td>
<td>DC 23 mV ±1 mV</td>
<td>Before turning ON power switch, turn VR02 fully counterclockwise. This bias current adjustment converts current value into voltage by Ohm's law.</td>
</tr>
<tr>
<td></td>
<td>Adj. L-ch</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>4.</td>
<td>Bias current</td>
<td>Between emitters of power transistors, TR702 &amp; TR704 (See Fig. 3-1)</td>
<td>VR02 (R-ch) F-2927</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>Adj. R-ch</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

3-2. Equalizer (F-3095) Adjustment (See Fig. 3-3)

<table>
<thead>
<tr>
<th>STEP</th>
<th>SUBJECT</th>
<th>MEASURE OUTPUT</th>
<th>ADJUST</th>
<th>ADJUST FOR</th>
<th>REMARKS</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.</td>
<td>DC 0V Adj.</td>
<td>Between GND &amp; TP</td>
<td>VR01 (L-ch) F-3095</td>
<td>DC 0V ±50 mV</td>
<td>Before turning ON power switch, set VR01 &amp; VR02 to center position.</td>
</tr>
<tr>
<td></td>
<td>L-ch</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2.</td>
<td>DC 0V Adj.</td>
<td>Between GND &amp; TP</td>
<td>VR02 (R-ch) F-3095</td>
<td></td>
<td></td>
</tr>
<tr>
<td></td>
<td>R-ch</td>
<td></td>
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</tr>
</tbody>
</table>

Fig. 3-1 Driver Circuit Board (F-2926, F-2927)

Fig. 3-2 Speaker Terminal

Fig. 3-3 Equalizer Circuit Board (F-3095)
4. PARTS LOCATION & PARTS LIST

4-1. F-2926 Driver Circuit Board (L-CH) (Stock No. 7572351)
4-2. F-2927 Driver Circuit Board (R-CH) (Stock No. 7572361)

Since some of capacitors and resistors are omitted from parts lists in this Service Manual, refer to the Common Parts List for capacitors & resistors which was appended previously to each Sansui Manual.
The circuit boards, F-3099, F-3100, F-3094 & F-3096 are not supplied as the assembled, the individual parts on the circuit boards, however are provided for orders.
6. SCHEMATIC DIAGRAM